OSMEVK_ADAP_MSMP2-V1.0

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Revision History

Revision	Date	Changes
1.0	28.08.2025	Initial release

OSMEVK_ADAP_MSMP2

Sheet: TITLE PAGE

Variant: MSMP2

Number 1

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Date: 28.08.2025

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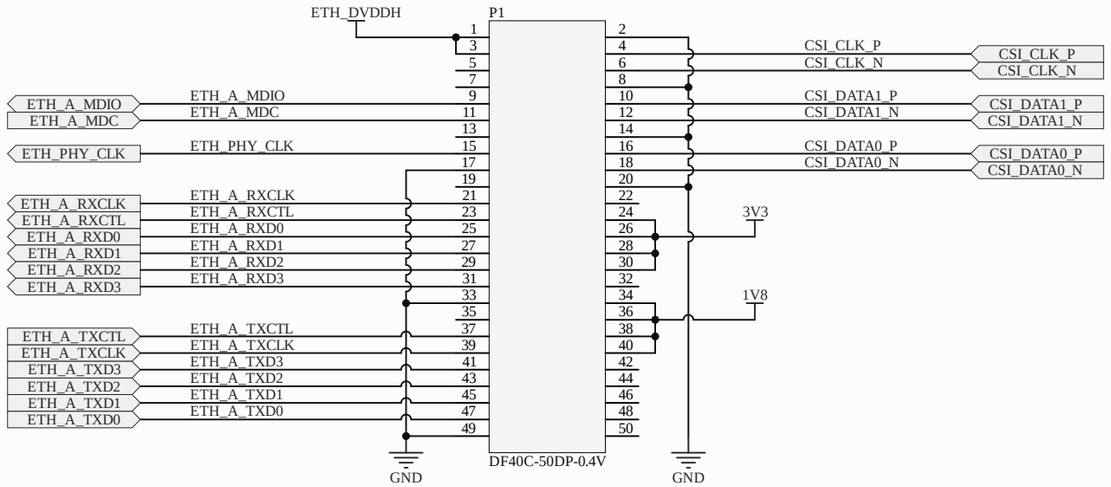

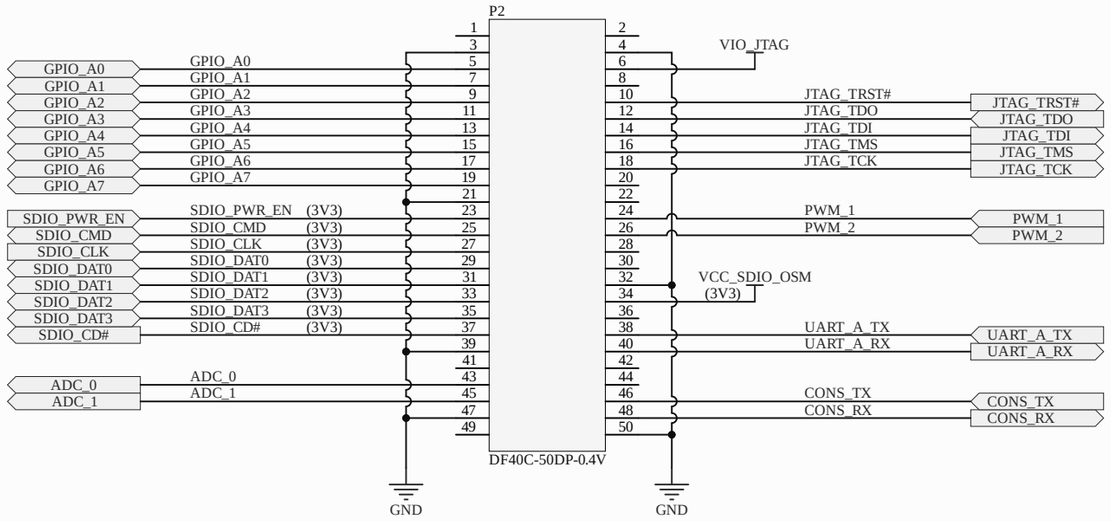

OSMEVK_ADAP_MSMP2	
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Variant: MSMP2	
Number 2	
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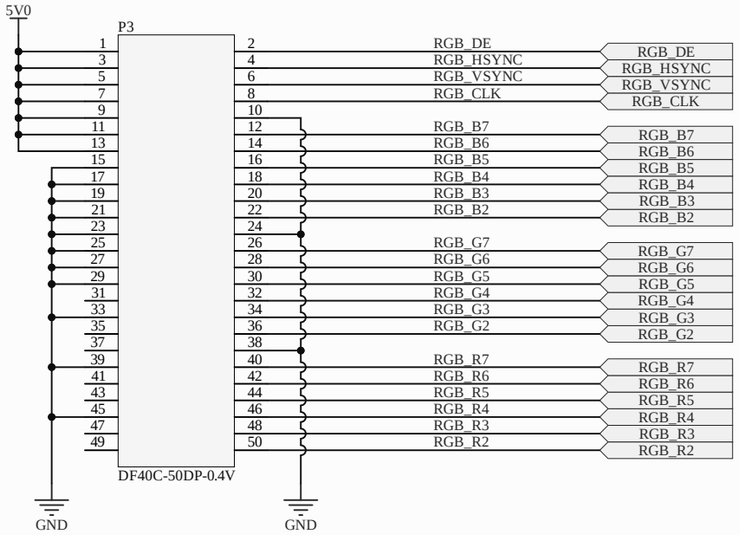

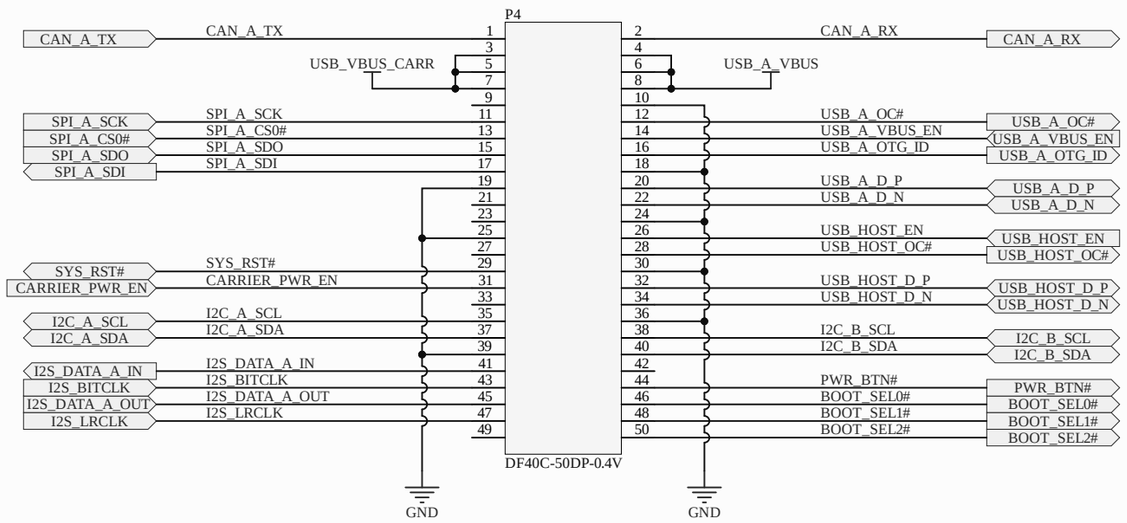

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Sheet: CONNECTOR BOTTOM	
Variant: MSMP2	
Number 3	
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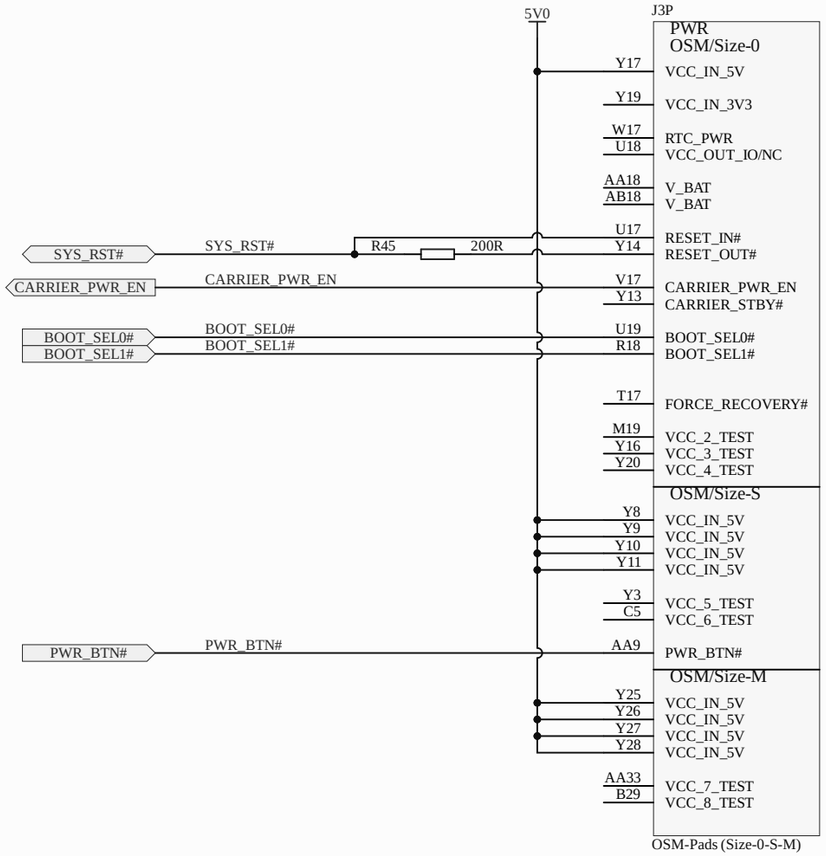
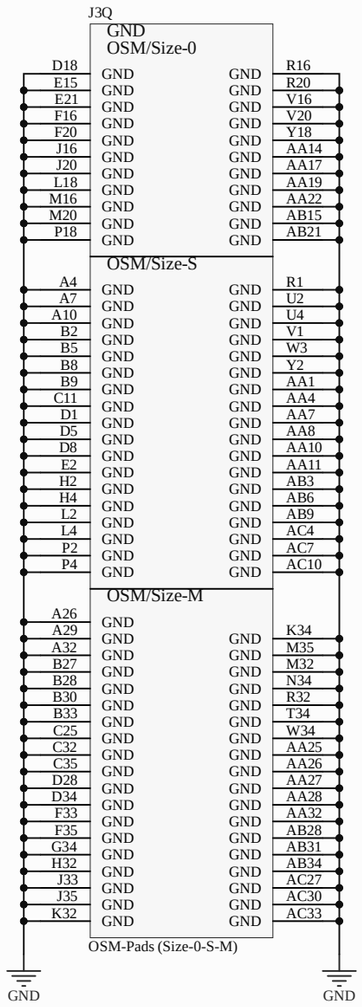
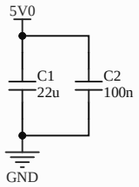
POWER/SYSTEM







OSMEVK_ADAP_MSMP2	
Sheet: POWER/SYSTEM	
Variant: MSMP2	
Number 4	
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ETHERNET

J3A

ETHERNET
OSM/Size-0

ETH_A_TXCLK	ETH_A_TXCLK	J15	ETH_A(R)(G)MII_TX_CLK
ETH_A_TXD0	ETH_A_TXD0	H15	ETH_A(S)(R)(G)MII_TXD0
ETH_A_TXD1	ETH_A_TXD1	G15	ETH_A(S)(R)(G)MII_TXD1
ETH_A_TXD2	ETH_A_TXD2	H16	ETH_A(S)(R)(G)MII_TXD2
ETH_A_TXD3	ETH_A_TXD3	G16	ETH_A(S)(R)(G)MII_TXD3
ETH_A_TXCTL	ETH_A_TXCTL	K16	ETH_A(R)(G)MII_TX_EN(ER)
ETH_A_RXCLK	ETH_A_RXCLK	R15	ETH_A(R)(G)MII_RX_CLK
ETH_A_RXD0	ETH_A_RXD0	K15	ETH_A(S)(R)(G)MII_RXD0
ETH_A_RXD1	ETH_A_RXD1	L15	ETH_A(S)(R)(G)MII_RXD1
ETH_A_RXD2	ETH_A_RXD2	N15	ETH_A(R)(G)MII_RXD2
ETH_A_RXD3	ETH_A_RXD3	P15	ETH_A(R)(G)MII_RXD3
		L16	ETH_A(R)(G)MII_RX_ER
ETH_A_RXCTL	ETH_A_RXCTL	M15	ETH_A(R)(G)MII_RX_DV(ER)
		E16	ETH_A(R)(G)MII_CRS
		F15	ETH_A(R)(G)MII_COL
ETH_PHY_CLK	ETH_PHY_CLK	N16	ETH_A_SDP
ETH_A_MDC	ETH_A_MDC	T16	ETH_MDC
ETH_A_MDIO	ETH_A_MDIO	T15	ETH_MDIO
	ETH_IOPWR	M17	ETH_IOPWR

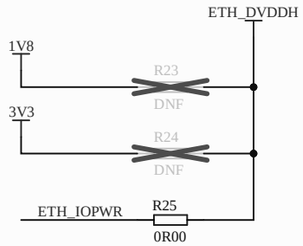
OSM/Size-S

H1	ETH_B(R)(G)MII_TX_CLK
G1	ETH_B(S)(R)(G)MII_TXD0
F1	ETH_B(S)(R)(G)MII_TXD1
G2	ETH_B(S)(R)(G)MII_TXD2
F2	ETH_B(S)(R)(G)MII_TXD3
J2	ETH_B(R)(G)MII_TX_EN(ER)
P1	ETH_B(R)(G)MII_RX_CLK
J1	ETH_B(S)(R)(G)MII_RXD0
K1	ETH_B(S)(R)(G)MII_RXD1
M1	ETH_B(R)(G)MII_RXD2
N1	ETH_B(R)(G)MII_RXD3
K2	ETH_B(R)(G)MII_RX_ER
L1	ETH_B(R)(G)MII_RX_DV(ER)
D2	ETH_B(R)(G)MII_CRS
E1	ETH_B(R)(G)MII_COL
M2	ETH_B_SDP
C6	ETH_B_MDC
C7	ETH_B_MDIO

OSM/Size-M

N35	ETH_C(R)(G)MII_TX_CLK
Y35	ETH_C(S)(R)(G)MII_TXD0
AA35	ETH_C(S)(R)(G)MII_TXD1
Y34	ETH_C(S)(R)(G)MII_TXD2
AA34	ETH_C(S)(R)(G)MII_TXD3
V34	ETH_C(R)(G)MII_TX_EN(ER)
W35	ETH_C(R)(G)MII_RX_CLK
V35	ETH_C(S)(R)(G)MII_RXD0
U35	ETH_C(S)(R)(G)MII_RXD1
R35	ETH_C(R)(G)MII_RXD2
P35	ETH_C(R)(G)MII_RXD3
U34	ETH_C(R)(G)MII_RX_ER
T35	ETH_C(R)(G)MII_RX_DV(ER)
AC34	ETH_C(R)(G)MII_CRS
AB35	ETH_C(R)(G)MII_COL
R34	ETH_C_SDP
N33	ETH_CDE_MDC
P33	ETH_CDE_MDIO

OSM-Pads (Size-0-S-M)



OSMEVK_ADAP_MSMP2	
Sheet: ETHERNET	
Variant: MSMP2	
Number 5	
Revision: 1.0	Sheet 5 of 9
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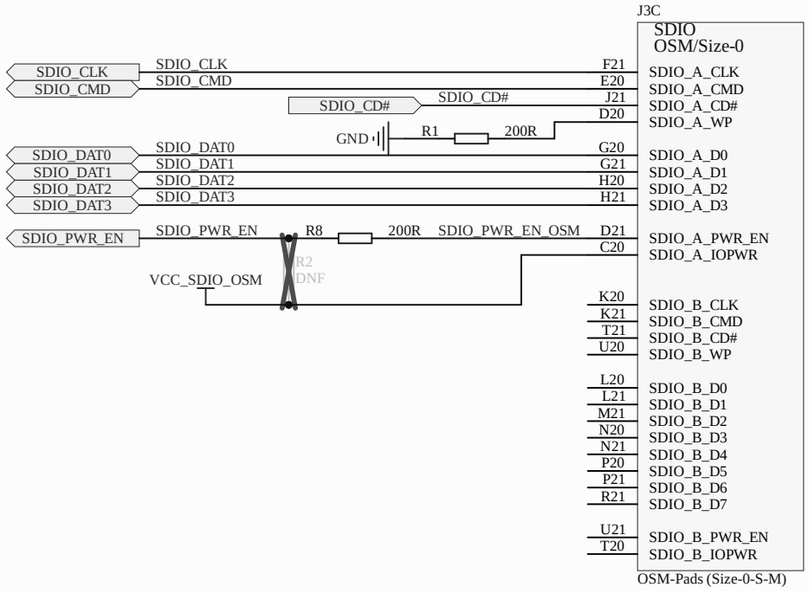
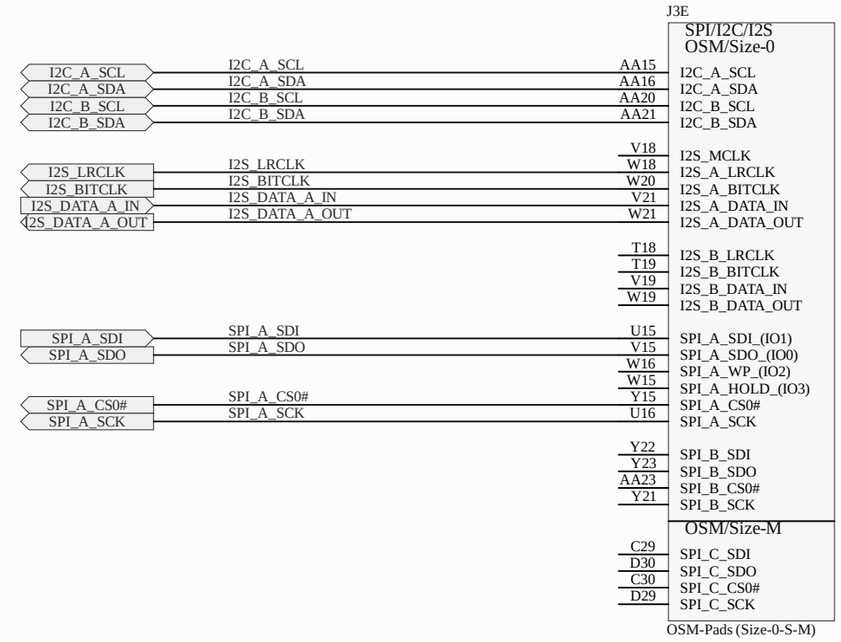
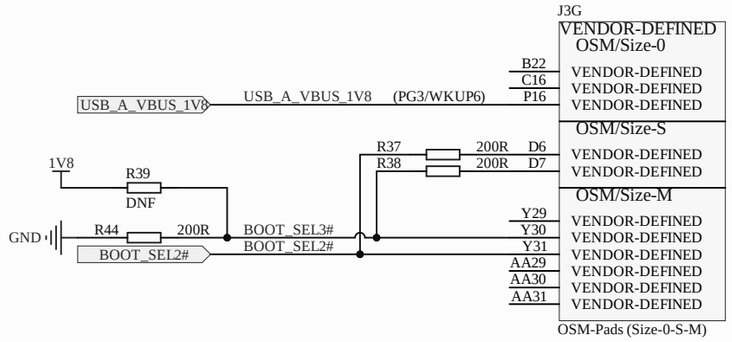
SDIO



I2C/I2S/SPI



VENDOR DEFINED



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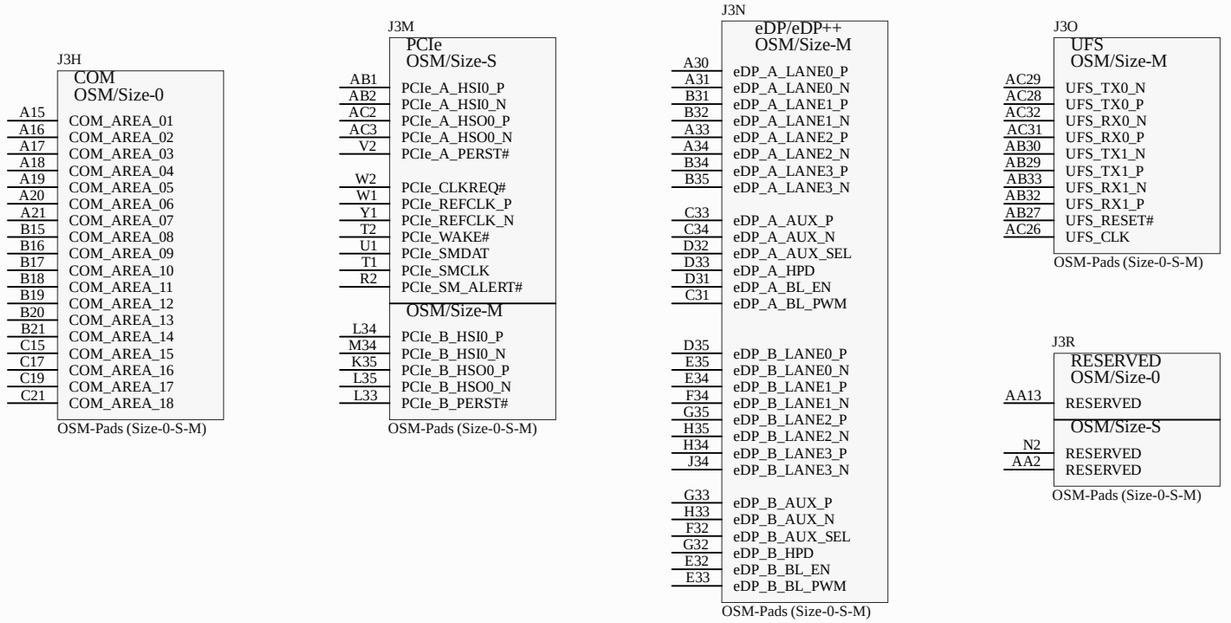

OSMEVK_ADAP_MSMP2	
Sheet:	I2C/SPI/I2S/SDIO
Variant:	MSMP2
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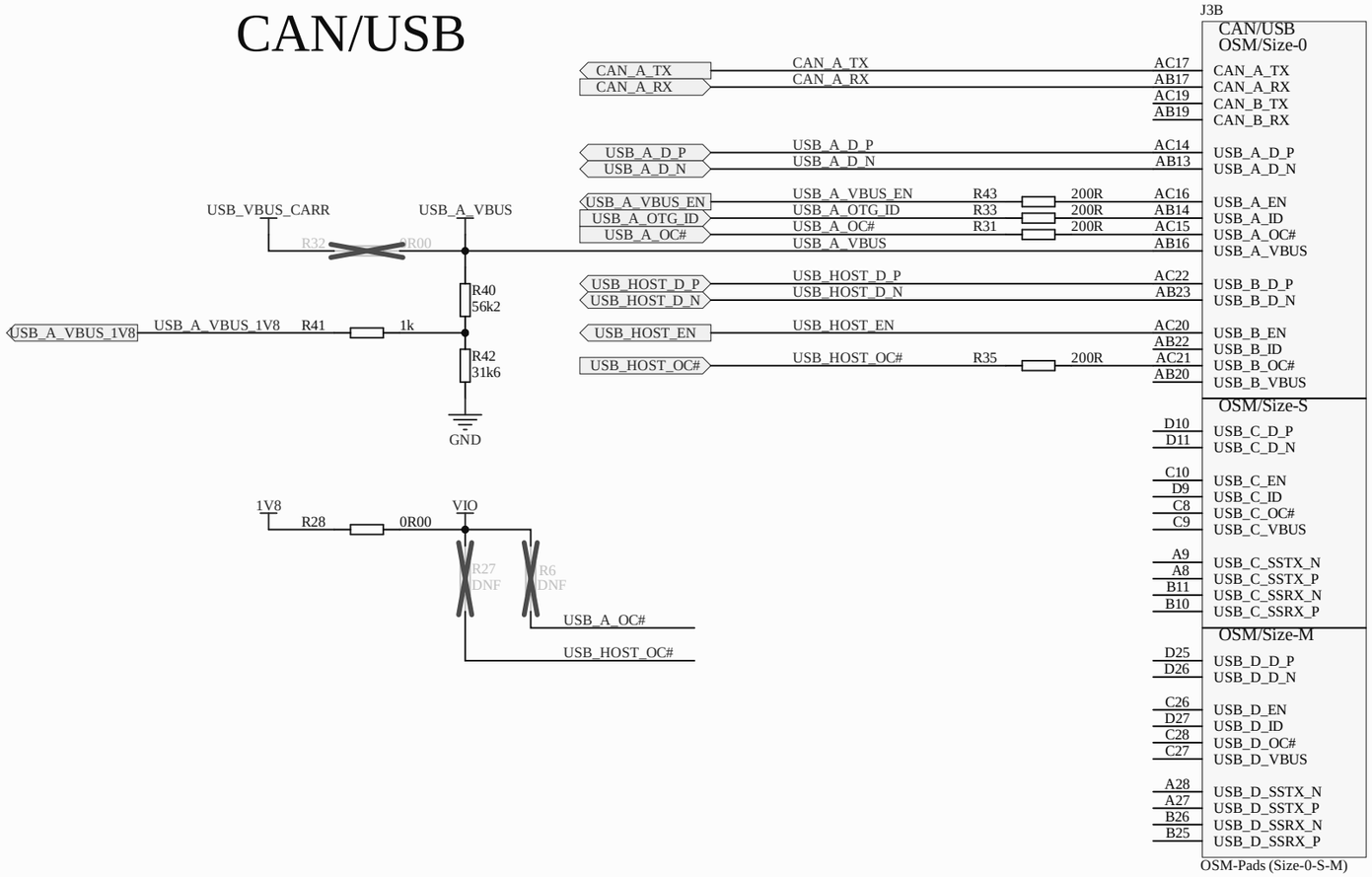
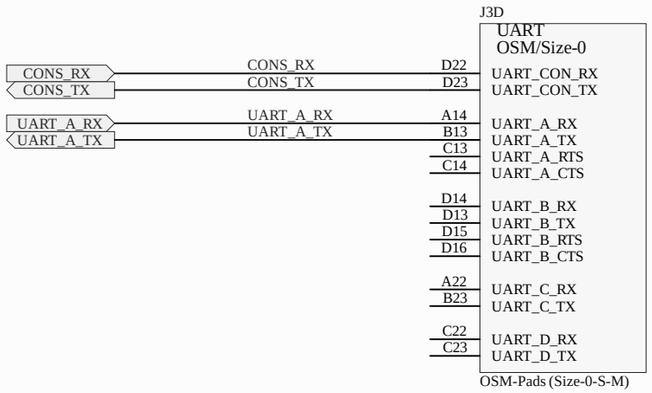
CAN/USB



UART



OSMEVK_ADAP_MSMP2	
Sheet: UART/USB/CAN	
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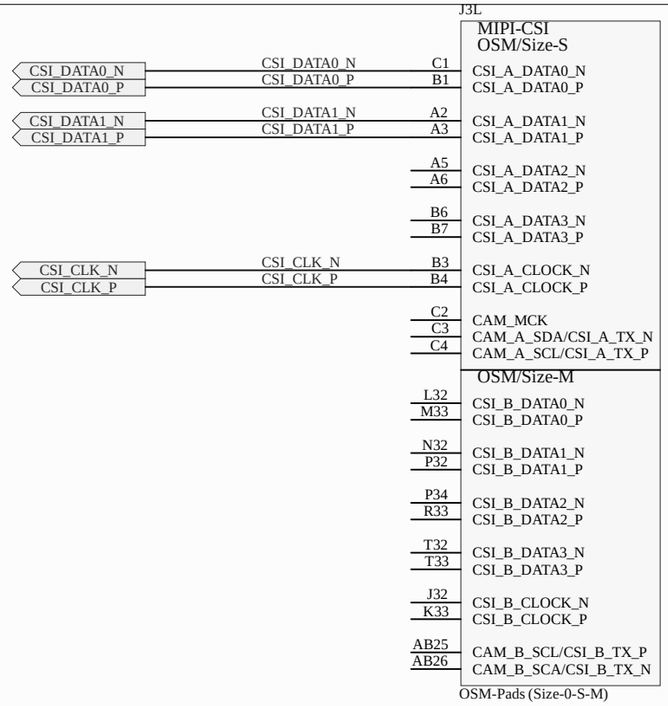
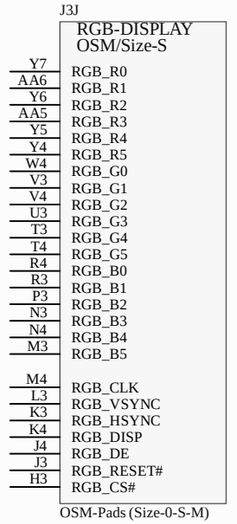
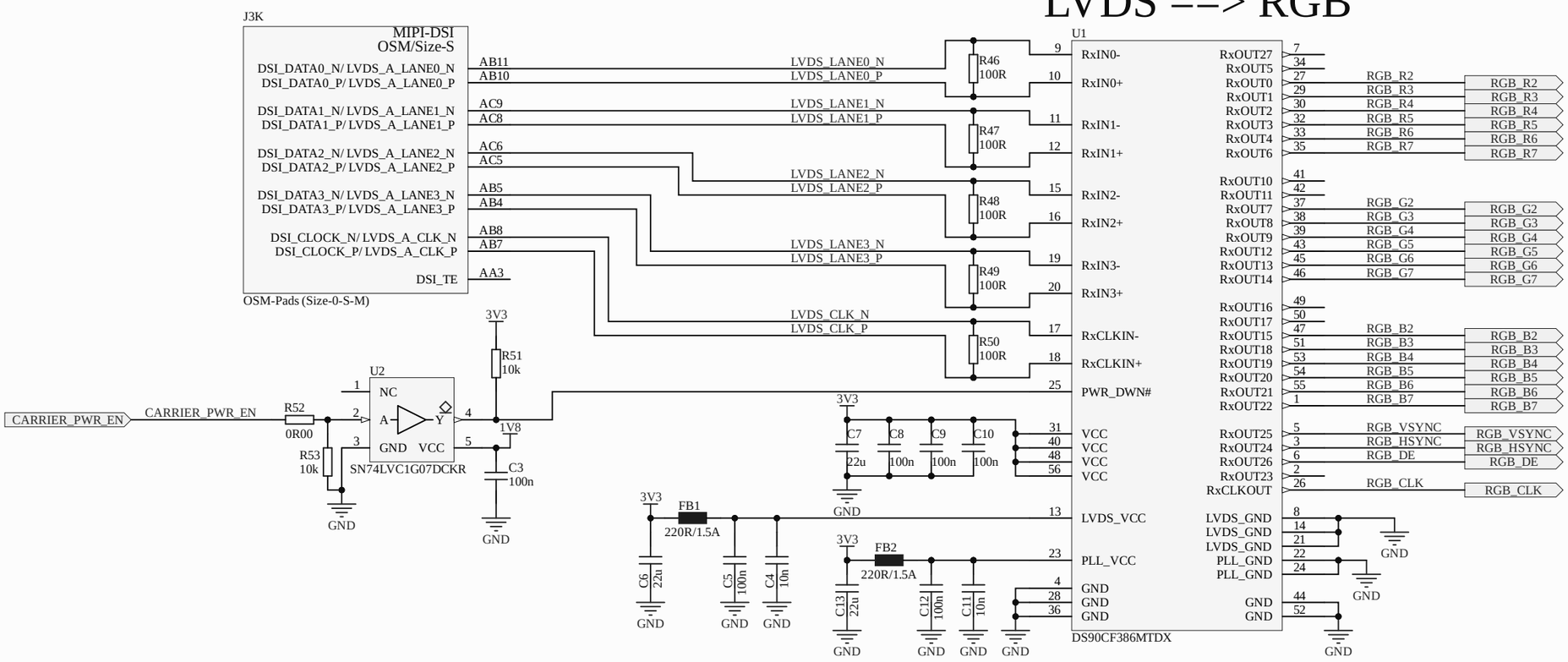
CSI



RGB



LVDS ==> RGB



OSMEVK_ADAP_MSMP2	
Sheet: CSI/DISPLAY	
Variant: MSMP2	
Number 8	
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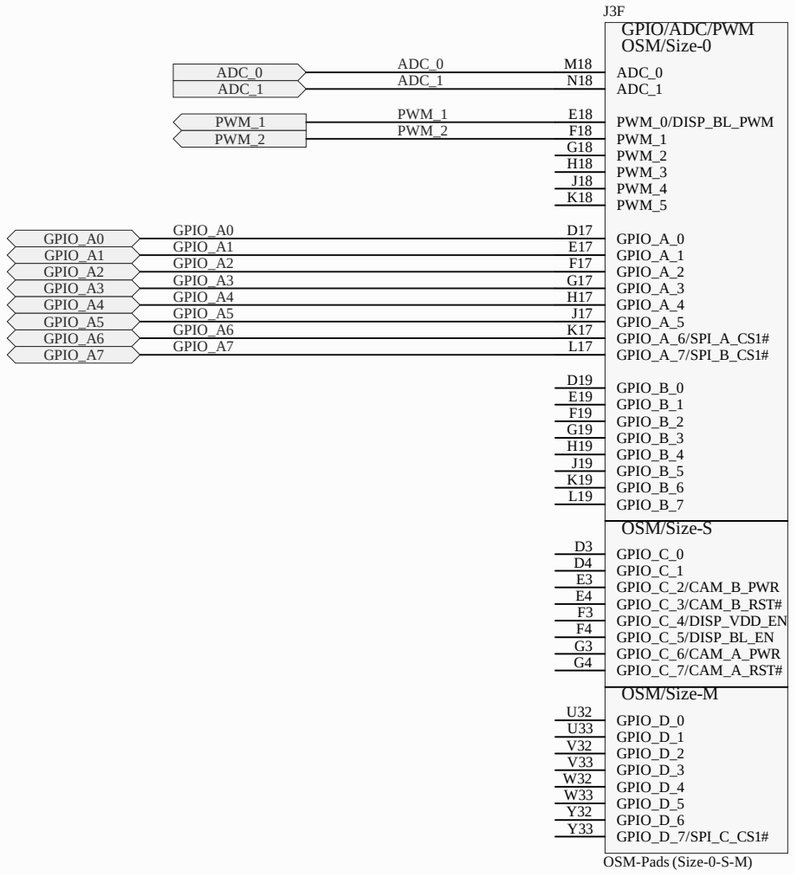
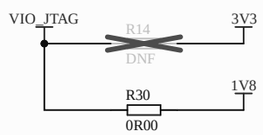
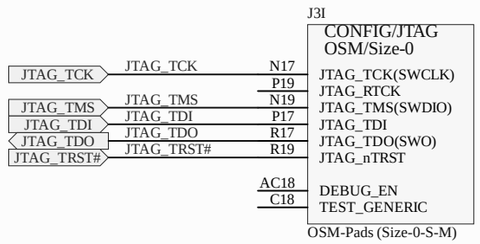
GPIO/PWM/ADC



JTAG





OSMEVK_ADAP_MSMP2	
Sheet: GPIO/PWM/ADC/JTAG	
Variant: MSMP2	
Number 9	
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