

MRZG2LS/MRZV2LS

SMARC Module

The MRZG2LS and MRZV2LS are the SMARC V2.1 compliant System-on-Modules based on Renesas RZ Family architecture offering high-performance for embedded systems.

The RZ/G2L microprocessor includes a Cortex®-A55 (1.2 GHz) CPU, 16-bit DDR3L / DDR4 interface, 3D graphics engine with Arm Mali-G31 and video codec (H.264). It also has many interfaces such as camera input, display output, USB 2.0, and Gbit-Ether, making it ideal for applications such as entry-class industrial human-machine interfaces (HMIs) and embedded devices with video capabilities.



MRZG2LS/MRZV2LS featureset:

- Single or Dual Cortex-A55, up to 1GHz
- optional AI accelerator; DRP-AI on MRZV2L
- 3D Graphics engine (Arm Mali-G31)
- Video Codec (H.264)
- Cortex-M33
- 512MB – 4GB DDR4 RAM
- SPI NOR
- 4GB-64GB eMMC NAND Flash
- dual 10/100/1000MBit Ethernet with PHY
- USB2.0 Host/OTG
- 2x CAN
- UART, I2C, SPI
- ADC
- Camera interface MIPI-CSI
- Display interface MIPI-DSI
- compliant to the SGET SMARC 2.1 standard
- commercial (0°C...+70°C) / industrial (-40°C...+85°C) temperature range



ARIES Embedded GmbH
Schöngesinger Str. 84
D-82256 Fürstfeldbruck
Germany

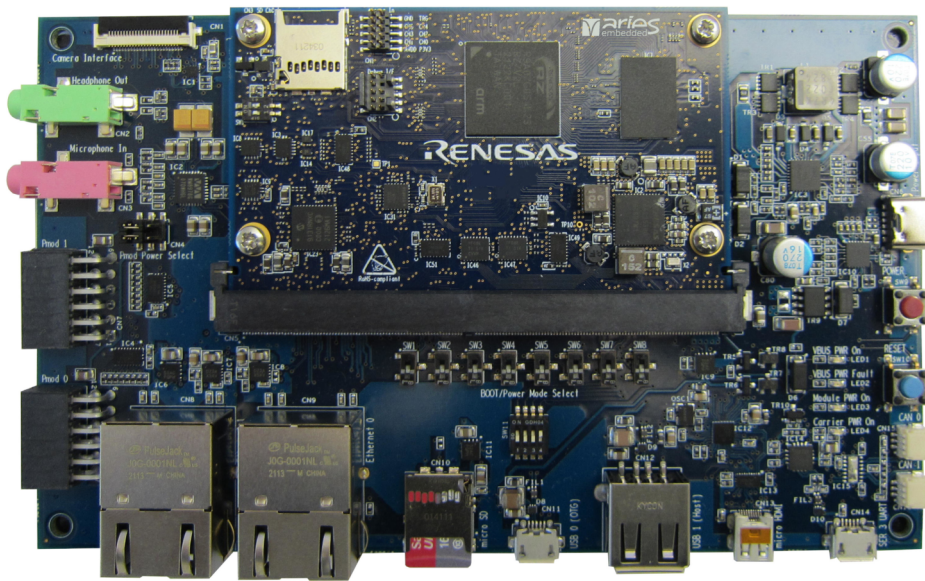
Tel: +49(0)8141.36 367-0
Fax: +49(0)8141.36 367-67

www.aries-embedded.com
info@aries-embedded.de

MRZG2LSEVK Evaluation Kit

The MRZG2LSEVK is the ideal platform for a quick and smooth startup with the new architecture. Due to its functionality it supports developers for a quick startup, helps developing software and can also be used as a platform for rapid prototyping.

The baseboard supports both the MRZG2LS and MRZV2LS System-on-Modules which are compliant to the SMARC 2.1 specification.



MRZG2LSEVK featureset:

- MRZG2LS or MRZV2LS System on Module
- 2x Ethernet on RJ45
- 2x CAN on DSub9
- MIPI-DSI via converter on HDMI connector
- MIPI-CSI on connector
- USB2.0 OTG on USB micro AB
- USB2.0 Host on USB A
- console via converter on USB micro AB
- UART, I2C, SPI on pin header
- SD-card on μ SD-card slot



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