

MCVS System on Module

MCVS offers the full flexibility of the Intel PSG Cyclone V SoC FPGA on a compact SMARC2.0 compatible SoM. It integrates an ARM-based hard processor system (HPS) consisting of processor, peripherals, and memory interfaces with the FPGA fabric using a high-bandwidth interconnect backbone. The Intel PSG SoCs combine the performance and power savings of hard intellectual property (IP) with the flexibility of programmable logic.



MCVS featureset:

- Altera Cyclone V SoC FPGA
- Dual 800 MHz Cortex A9 Cores
- 256 Mbit Configuration Device
- rich HPS and FPGA I/O
- programmable clock generator/PLL
- HPS-Peripherals:
 - 1 GiB DDR3 RAM
 - 4 GiB ... 64 GiB eMMC NAND Flash
 - 2 x Gigabit Ethernet with PHY
 - UART
 - CAN
 - SPI
 - I2C
 - USB OTG
- single 3,0V-5,5V supply
- 314 pins edge connector
- SMARC2.0 compliant
- Size: 82mm x 50mm
- commercial (0°C...+70°C) / industrial (-40°C...+85°C) temperature range

OS Support:

- U-Boot
- Linux



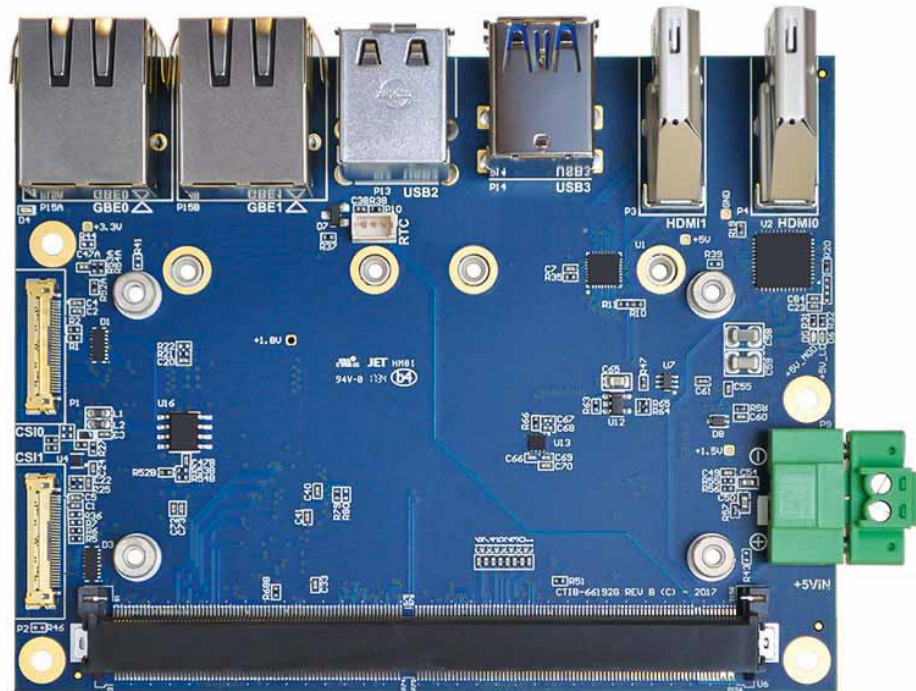
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MCVSBB Baseboard

The MCVSBB offers a SMARC2.0 compliant baseboard solution for the MCVS SoM. As many customers target to use MCVS in an industrial environment the MCVSBB can easily be used as a platform for fast prototyping.



MCVSBB featureset:

- 2x HPS Gigabit Ethernet
- HPS USB
- 1xUART
- 2x HDMI
- TFT with Touch

MCVSEVP kit contents:

- MCVS-2DB featuring 5CSEBA2U23C8N, 1GB DDR3 RAM, 4GB e.MMC NAND Flash
- MCVSBB baseboard
- 17,7cm (7") LCD with touchscreen (optional)
- power supply



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