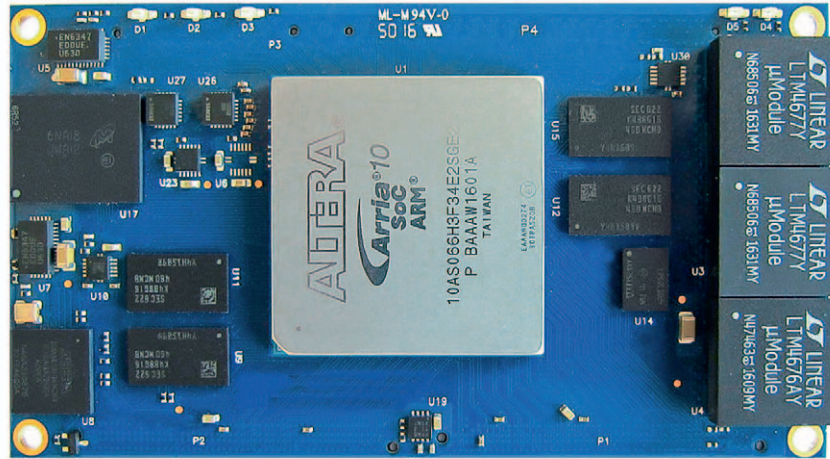


MAX System on Module

The MAX module provides a complete System based on Intel® Arria®10 FPGA family.

The Arria 10 SoCs integrate an ARM-based hard processor system (HPS) consisting of processor, peripherals, and memory interfaces with the FPGA fabric using a high-bandwidth interconnect backbone. It combines the performance and power savings of hard intellectual property (IP) with the flexibility of programmable logic.



MAX featureset:

- Arria 10 Soc FPGA (10AS066/ 10AS048)
- Dual Cortex A9 Cores (up to 1.5GHz)
- HPS-Peripherals
- 2x DDR3 Memory bank with 1/ 2/ 4 Gbyte RAM
- 1 Gbit configuration device
- eMMC NAND Flash (4/ 16/ 64 Gbyte)
- 24 transceiver channel
- 232 FPGA I/O pins
- 52 HPS I/O pins
- 2 user LEDs
- 12V power supply
- Size: 60mm x 110mm

OS Support:

- U-Boot
- Linux



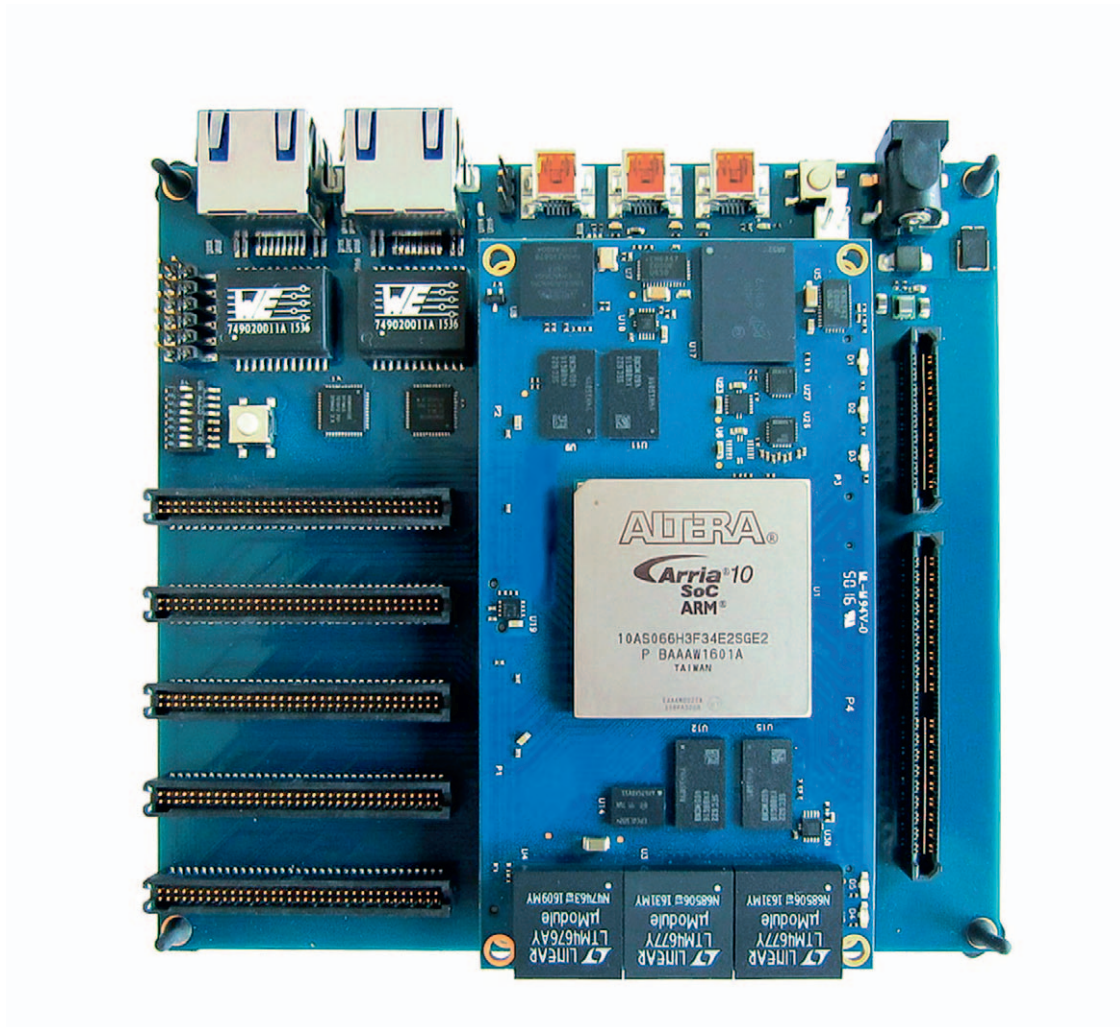
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MAX Evaluation Kit

The MAXEVK is a baseboard for the MAX module. It provides several interfaces for the HPS Subsystem and all required FPGA IO pins.



MAXEVK featureset:

- 2 x Gigabit Ethernet
- USB-OTG
- UART / USB converter
- embedded USB-Blaster II
- 5 x pinheader for FPGA / HPS signals
- 2 x high-speed connectors for 24 transceiver
- 12V power supply
- clock generation for Ethernet / USB
- wall plug power supply
- Size: 180mm x 180mm

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